PATENT NUMBER.

418 418 	•	Subclass	ISSUE CLASSIFICATION
109/7234		Class	ISSUE CLA

	Subclass	SSUE CLASSIFICATION
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U.S.	UTILITY	Patent A	pplication
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O.I.P.E. PATENT DATE	 _
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	09/723418	CONT/PRIOR		SUBCLASS	ART UNIT	EXAMINER HALL
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High performance ipsec hardware accelerator for packet

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)			
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INTERNATIONAL CLASSIFICATION					
		Continued on Issue Slip Inside File Jacket			

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED		
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G	
The term of this patent subsequent to(date)				NOTICE OF ALLOWANCE MAILED		
has been disclaimed.	(Assistant E	xaminer)	(Date)			
The term of this patent shall not extend beyond the expiration date of U.S Patent. No				ISSUE FEE		
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				Amount Due	Date Paid	
	(Primary Examiner) (Date		(Date)		•	
The terminalmonths of this patent have been disclaimed.				ISSUE BATCH NUMBER		
	(Legal Instrumen	ts Examiner	(Date)		_	

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